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APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/911,840	07/23/2001	Scott Cumeralto	1725.123US02	3896
24113	7590 08/18/2006		EXAMINER	
	N, THUENTE, SKAA	TRAN, KHANH C		
4800 IDS CENTER 80 SOUTH 8TH STREET		ART UNIT	PAPER NUMBER	
MINNEAPOLIS, MN 55402-2100			2611	
			DATE MAILED: 08/18/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	on No.	Applicant(s)					
		09/911,84	10	CUMERALTO ET AL.					
	Office Action Summary	Examiner		Art Unit					
		Khanh Tra	ın	2611					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply									
A SH WHIC - Exter after - If NC - Failu Any r	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAILInsions of time may be available under the provisions of 37 SIX (6) MONTHS from the mailing date of this communical period for reply is specified above, the maximum statutor re to reply within the set or extended period for reply will, be reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	ING DATE OF TH CFR 1.136(a). In no evi- tition. y period will apply and w by statute, cause the app	HIS COMMUNICATION ent, however, may a reply be timil expire SIX (6) MONTHS from lication to become ABANDONE	I. nely filed the mailing date of this co D (35 U.S.C. § 133).					
Status									
2a)⊠	Responsive to communication(s) filed or This action is FINAL . 2b)[Since this application is in condition for a closed in accordance with the practice upon the condition for a closed in accordance with the practice upon the closed in	☐ This action is nallowance except	for formal matters, pro		e merits is				
Dispositi	on of Claims								
5)⊠ 6)⊠ 7)□ 8)□ Applicati	Claim(s) 15-28 is/are pending in the app 4a) Of the above claim(s) is/are w Claim(s) 15,16,22 and 23 is/are allowed Claim(s) 17-21 and 24-28 is/are rejected Claim(s) is/are objected to. Claim(s) are subject to restriction on Papers	ithdrawn from co d. and/or election r							
10)⊠	The specification is objected to by the Ex The drawing(s) filed on 23 July 2001 is/a Applicant may not request that any objection Replacement drawing sheet(s) including the The oath or declaration is objected to by	re: a)⊠ accepte to the drawing(s) t correction is requir	e held in abeyance. See ed if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CF					
Priority L	ınder 35 U.S.C. § 119								
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 									
2)	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-9 nation Disclosure Statement(s) (PTO-1449 or PTO- r No(s)/Mail Date		4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	ite)-152)				

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DETAILED ACTION

1. The Amendment filed on 06/09/2006 has been entered. Claims 15-28 are pending in this Office action.

Response to Arguments

2. Applicant's arguments, see page 8 of Applicants' Remarks, filed on 06/09/2006, with respect to the rejection(s) of claim(s) 8-10 and 13 under 35 U.S.C. 103(a) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn after Applicants cancelled on the rejected claims. However, upon further consideration, a new ground(s) of rejection is made in view of Partyka U.S. Patent 6,188,715 B1.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 17-21 and 24-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Partyka U.S. Patent 6,188,715 B1.

Regarding claim 17, Partyka invention is directed to a radio transmission system including many radio transmitters that use frequency hopping carrier to intermittently transmit very short messages indicative of status of sensors associated with the transmitters, and to provide a synchronization means and method that allows a receiver to receive messages from all the frequency hoping transmitters; see column 4 lines 25-35.

In column 17 lines 20-67, FIG. 2 illustrates a block diagram of a receiver including a control logic 190 comprising: (a) a plurality of ID memory registers 134 to hold digital data indicative of ID numbers for each transmitter that belongs to the system, (b) a plurality of time memory registers 136 to hold digital data indicative of the time of the next transmission occurrence for each respective transmitter, and (c) a plurality of frequency memory registers 138 to hold digital data indicative of the frequency of the next transmission occurrence for each respective transmitter.

In operation, Partyka teaches that the receiver control logic 190 (also shown in FIG. 5) sequentially compares the data content of the time registers 136 with the data content of the receiver timer 132 and if the transmission is due from a transmitter, the control logic programs the frequency selective radio receiver demodulator circuit according to the data content in the frequency register 138 for this transmitter, attempts to decode the demodulated signal, changes the content of the time register based on the number representative of the time interval between the transmissions for this transmitter and changes the content of the frequency register according to a predetermined algorithm for this transmitter. In light of the foregoing teachings, the act

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of comparing the data content of the time registers 136 with the data content of the receiver timer 132 to determine if the transmission is due from a transmitter corresponds to the step of determining a message arrival time as set forth in the application claim.

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Partyka does not explicitly teach the claimed step of activating a receiver as set forth in the claimed invention. However, as recited above, because the control logic 190 programs the frequency selective radio receiver demodulator circuit if the transmission is due from a transmitter as a result of the comparison, one of ordinary skill in the art at the time the invention was made would have recognized that the act of programming the frequency selective radio receiver demodulator circuit if the transmission is due from a transmitter as a result of the comparison corresponds to the step of activating a receiver of the first reader as claimed.

Regarding claim 18, as recited in claim 17, the control logic 190 comprising: (a) a plurality of ID memory registers 134 to hold digital data indicative of ID numbers for each transmitter that belongs to the system, (b) a plurality of time memory registers 136 to hold digital data indicative of the time of the next transmission occurrence for each respective transmitter, and (c) a plurality of frequency memory registers 138 to hold digital data indicative of the frequency of the next transmission occurrence for each respective transmitter.

Regarding claim 19, in column 18 lines 15-45, Partyka further teaches in one embodiment that if the timer resolution is 0.3 ms, then the next transmission time can be predicted with accuracy 0.3 ms. The receiver can program its frequency 0.3 ms in advance to each new frequency.

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Regarding claim 20, as recited in claim 17, the transmitters use frequency hopping carrier to intermittently transmit very short messages indicative of status of sensors associated with the transmitters. The receiver as shown in FIG. 2 includes a plurality of frequency memory registers 138 to hold digital data indicative of the frequency of the next transmission occurrence for each respective transmitter.

Regarding claim 21, in column 17 lines 5-25, the receiver as shown in FIG. 2 further includes a multi-channel RSSI function is to assist the controller with obtaining initial synchronization and to reacquire synchronization with transmitters with whom the synchronization has been lost.

Regarding claim 24, claim is rejected on the same ground as for claim 17 because of similar scope.

Regarding claim 25, the receiver as shown in FIG. 2 is a base unit.

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Regarding claim 26, claim is rejected on the same ground as for claim 18 because of similar scope.

Regarding claim 27, claim is rejected on the same ground as for claim 19 because of similar scope.

Regarding claim 28, claim is rejected on the same ground as for claim 21 because of similar scope.

Allowable Subject Matter

4. Claims 15-16 are allowed.

The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 15, claim 15 is allowable over prior art of record because the cited references (e.g. Partyka U.S. Patent 6,188,715 B1) do not teach or suggest the uniquely distinct features "predicting, by the receiver, whether the future second transmission will be unsuccessful based on the first set of determined frequency hopping information" and "responding to a predicted unsuccessful future second transmission by adjusting the communicating between the first end point module and the receiver".

5. Claims 22-23 are allowed.

The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 22, claim 22 is allowable over prior art of record because the cited references (e.g. Partyka U.S. Patent 6,188,715 B1) do not teach or suggest the uniquely distinct features "predict whether the future second transmission will be unsuccessful based on the first set of determined frequency hopping information" and "respond to a predicted unsuccessful future second transmission by adjusting the radio communications between the first end point module".

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Partyka U.S. Patent 6,535,544 B1 discloses "Frequency Hopping System For Intermittent Transmission".

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh Tran whose telephone number is 571-272-3007. The examiner can normally be reached on Monday - Friday from 08:00 AM - 05:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Primary Examiner KHANH TRAN